L Number	Hits	Search Text	DB	Time stamp
2	804	711/103.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 12:59
3	177	711/103.ccls. and (flash same transfer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 13:03
4	115	711/103.ccls. and (flash same transfer same control$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 13:51
5	35510	flash adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 13:51
6	2691	(flash adj memory) near2 (array or module or component or units)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:06
7	172	((flash adj memory) near2 (array or module or component or units)) with controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 13:53
11	0	((flash adj memory) near2 (array or module or component or units)) same (partition with transfer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 13:54
10	121	((flash adj memory) near2 (array or module or component or units)) same (partition or transfer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:07
12	3649	(flash adj memory) near2 (arra$2 or modul$2 or componen$2 or uni$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:07
13	209	((flash adj memory) near2 (arra$2 or modul$2 or componen$2 or uni$2)) same (partition or transfer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:29
14	187	(((flash adj memory) near2 (arra$2 or modul$2 or componen$2 or uni$2)) same (partition or transfer)) and control$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:15
15	16	((flash adj memory) near2 (arra$2 or modul$2 or componen$2 or uni$2)) same (partition)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 14:56
16	349	flash adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 15:36
17	27	(flash adj controller) and 711/103.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/16 15:36

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📄 shared memory protocols
   Rohit Chandra , Kourosh Gharachorloo , Vijayaraghavan Soundararajan , Anoop Gupta
   Proceedings of the 8th international conference on Supercomputing July 1994
      Hardware distributed shared memory (DSM) systems efficiently support fine grain
      sharing of data by maintaining coherence at the level of individual cache lines and
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